

Introducing State-Trajectory Control for the Synchronous Interleaved Boost Converter

Rafael Peña-Alzola, Peter Ksiazek and Martin Ordóñez

Department of Electrical and Computer Engineering

The University of British Columbia

Vancouver, BC V6T 1Z4 Canada

E-mails: rafaelpa@ece.ubc.ca

peter.ksiazek@ieee.org mordonez@ieee.org

Huai Wang and Frede Blaabjerg

Department of Energy Technology

Aalborg University

DK-9220 Aalborg East, Denmark

E-mails: hwa@et.aau.dk

fbl@et.aau.dk

Abstract—Synchronous interleaved boost converters (SIBCs) result in lower ripple currents and bidirectional power flow. The boost topology has a non-minimum phase characteristic, producing instability problems when a large bandwidth is required. Linear controllers inherently limit the boost controller bandwidth, resulting in a slow response. In this paper, state-trajectory control of the SIBC based on boundary control is proposed to provide an outstanding dynamic response during start-up and sudden load changes, close to the physical limit of the system. The proposed controller and derivation provides a rigorous framework that deals with four switching states, and three state equations, resulting in a simple control law with very fast dynamic response. The normalized trajectories for the SIBC are determined in the geometric domain along with the control law. The exact trajectories are used for fast transients, and approximate trajectories are employed for constant frequency in steady-state. Simulation and experimental results are provided to validate the proposed procedures.

I. INTRODUCTION

The basic boost topology has a non-minimum phase characteristic, producing instability problems [1] when a large bandwidth is required. Linear controllers inherently limit the boost controller bandwidth [2], resulting in a slow response as shown in Fig. 1. In order to overcome these issues, non-linear techniques, such as sliding mode [3]–[5] and boundary control [6]–[9] have been proposed. Boundary control provides an outstanding dynamic response during start-up and sudden load changes [6], [9]. Since the dynamic response is close to the theoretical optimum, steady state can be achieved with very few switching actions [7], [9]. These fast dynamics may enable the use of film capacitors instead of electrolytic capacitors, which would significantly increase the reliability of the system [10]. Boundary control uses the trajectories of the state variables in the phase-plane (geometric) to obtain a control law [11], [12]. The previous literature review [6]–[8], [11]–[14] indicates that there is an abundance of articles on boundary control for the regular boost converter, but no studies on boundary control of the interleaved boost converter. Boundary control of the SIBC results in increased complexity, with an additional state equation necessitated by the additional storage element, and it is addressed in this work. Additionally, as shown in Fig. 1, the number of switching states is doubled (four switching states of SIBC versus two switching states

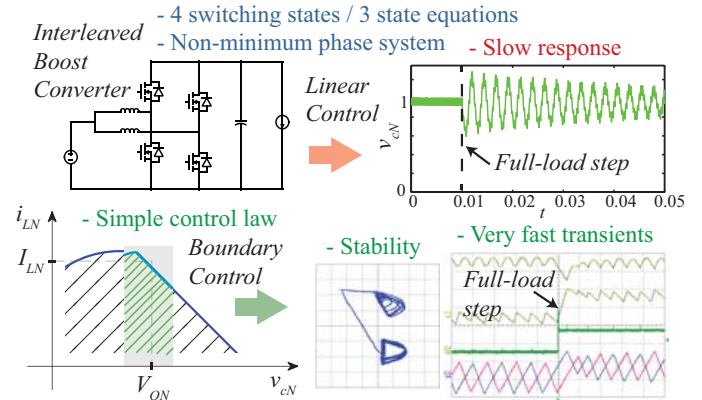


Fig. 1. Conceptual procedure and results of the proposed boundary control for the SIBC.

of simple boost converters). The proposed controller and derivation provides a rigorous framework that deals with the additional states and equations, resulting in a simple control law with very fast dynamic response, which is conceptually depicted in Fig. 1. The normalized trajectories for the SIBC are determined in the geometric domain along with the control law. The exact trajectories are used for fast transients, and approximate trajectories are employed for constant frequency in steady-state. Simulation and experimental results are provided to validate the proposals.

II. NORMALIZED STATE TRAJECTORIES

Fig. 2 shows the bidirectional SIBC; each inductor has an inductance of $2L$, so the total parallel inductance is L . The load is a constant current source I_O and the input voltage V_I is considered constant (capital letters will denote constant magnitudes). There are four different switching states in the SIBC, with the switches S_1 and S_2 being 1/0 when on/off. The lower switches are complementary to the upper ones. The equations of the boost converter depend on the switching state of the power devices S_1 and S_2 according to:

$$C \frac{dv_c}{dt} = \bar{S}_1 i_{L1} + \bar{S}_2 i_{L2} - I_O \quad (1a)$$

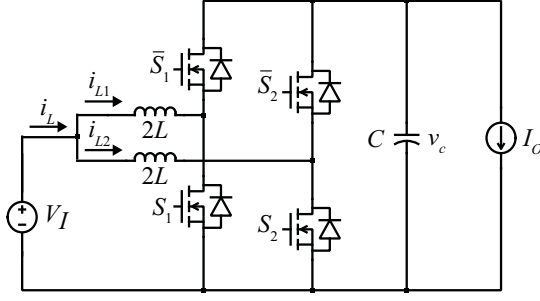


Fig. 2. Synchronous Interleaved boost converter (SIBC).

$$2L \frac{di_{L1}}{dt} = V_I - \bar{S}_1 v_c \quad (1b)$$

$$2L \frac{di_{L2}}{dt} = V_I - \bar{S}_2 v_c \quad (1c)$$

with v_c the output voltage, C the output capacitor capacitance, and finally i_{L1} and i_{L2} the currents through the different inductors. In order to derive a simple control law, it is convenient to define the total current $i_L = i_{L1} + i_{L2}$ and the current difference $\Delta i_L = i_{L1} - i_{L2}$. It is also convenient to normalize the voltage, current, and time, with $V_b = V_{rated}$ the output rated voltage, $I_b = V_{ref} \sqrt{C/L}$ and $T_b = \sqrt{LC}$ respectively [13]. With this normalization $C_N = L_N = 1$, which allows for simpler formulas and lower computations for the control law implementation. For the switching states $S_1 = S_2$, the following equations result:

$$\left. \begin{aligned} \frac{dv_{cN}}{dt_N} &= i_{LN} - I_{ON} \\ \frac{di_{LN}}{dt_N} &= V_{IN} - v_{cN} \\ 2 \frac{d\Delta i_{LN}}{dt_N} &= 0 \end{aligned} \right\} \text{ for } S_1 = S_2 = 0 \quad (2)$$

$$\left. \begin{aligned} \frac{dv_{cN}}{dt_N} &= -I_{ON} \\ \frac{di_{LN}}{dt_N} &= V_{IN} \\ 2 \frac{d\Delta i_{LN}}{dt_N} &= 0 \end{aligned} \right\} \text{ for } S_1 = S_2 = 1 \quad (3)$$

The subscript N denotes normalized variables. The selected normalization presents another interesting property, namely that the trajectories for $S_1 = S_2 = 0$ are circles, with their centres at (I_{ON}, V_{IN}) , rotating with unity angular speed, see Fig. 3:

$S_1 = S_2 = 0$:

$$\begin{aligned} (i_{LN} - I_{ON})^2 + (v_{cN} - V_{IN})^2 \\ = (I_{LBN} - I_{ON})^2 + (V_{cBN} - V_{IN})^2 \end{aligned} \quad (4)$$

with I_{LBN} , ΔI_{LBN} and V_{cBN} the initial values. The trajectories for $S_1 = S_2 = 1$ are straight lines, see Fig. 3, [13]:

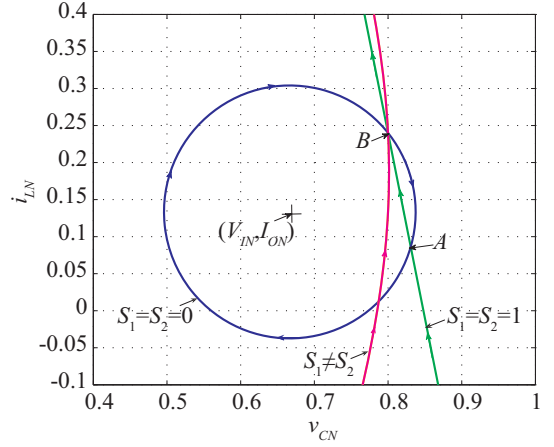


Fig. 3. Trajectories for the different switching states.

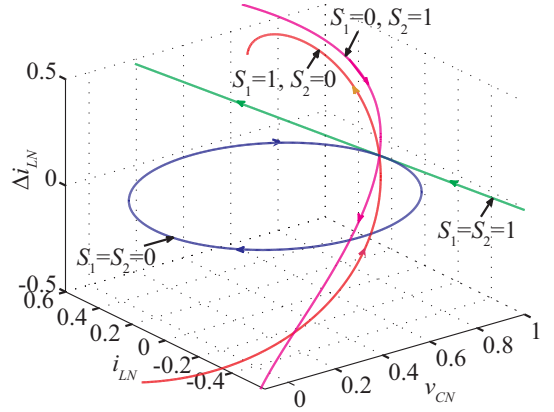


Fig. 4. Trajectories for the different switching states including Δi_{LN} .

$S_1 = S_2 = 1$:

$$(i_{LN} - I_{LNB}) = -V_{IN}/I_{ON}(V_{cN} - V_{cBN})^2 \quad (5)$$

It is clear that, for these cases, the SIBC behaves as a simple boost converter. Moreover, the current difference Δi_L does not change for this switching states $S_1 = S_2$, see (2)-(3). For $S_1 \neq S_2$, the following equations result:

$$\left. \begin{aligned} \frac{dv_{cN}}{dt_N} &= \frac{1}{2}(i_{LN} - \Delta i_{LN}) - I_{ON} \\ \frac{di_{LN}}{dt_N} &= V_{IN} - \frac{v_{cN}}{2} \\ 2 \frac{d\Delta i_{LN}}{dt_N} &= v_{cN} \end{aligned} \right\} \text{ for } S_1 = 1, S_2 = 0 \quad (6)$$

$$\left. \begin{aligned} \frac{dv_{cN}}{dt_N} &= \frac{1}{2}(i_{LN} + \Delta i_{LN}) - I_{ON} \\ \frac{di_{LN}}{dt_N} &= V_{IN} - \frac{v_{cN}}{2} \\ 2 \frac{d\Delta i_{LN}}{dt_N} &= -v_{cN} \end{aligned} \right\} \text{ for } S_1 = 0, S_2 = 1 \quad (7)$$

The second equation in (6) and (7) shows that the behavior of i_{LN} , increasing or decreasing, depends on whether V_{IN} is higher or lower than $v_{cN}/2$. The third equation in (6) and (7) shows that Δi_{LN} varies when these switching states $S_1 \neq S_2$ are applied. By eliminating i_{LN} and Δi_{LN} in the first equation of the previous sets, it results that:

$$\left. \begin{aligned} 2\frac{d^2 v_{cN}}{dt_N^2} + v_{cN} &= V_{IN} \\ \frac{di_{LN}}{dt_N} &= V_{IN} - \frac{v_{cN}}{2} \\ 2\frac{d\Delta i_{LN}}{dt} &= v_{cN} \end{aligned} \right\} \text{for } S_1 = 1, S_2 = 0 \quad (8)$$

$$\left. \begin{aligned} 2\frac{d^2 v_{cN}}{dt_N^2} + v_{cN} &= V_{IN} \\ \frac{di_{LN}}{dt_N} &= V_{IN} - \frac{v_{cN}}{2} \\ 2\frac{d\Delta i_{LN}}{dt} &= -v_{cN} \end{aligned} \right\} \text{for } S_1 = 0, S_2 = 1 \quad (9)$$

Therefore, according to (8) and (9), the switching states $S_1 \neq S_2$ produce the same effect for i_{LN} , but the opposite for Δi_{LN} . Moreover, the trajectories in the phase plane of v_{cN} and i_{LN} for $S_1 \neq S_2$ will be the same, see Fig. 3, provided that the initial values are the same. In the normalized domain, the phase plane trajectories of these equations result in a circle rotating at a constant speed for i_{LN1} and a straight line for i_{LN2} . The values for i_{LN} and Δi_{LN} result from the combination of i_{L1} and i_{L2} , but the trajectory equations do not result in simple geometrical forms, see Fig. 3. The three dimensional trajectories for the different switching states are shown in Fig. 4. It can be seen that, unlike the trajectories for $S_1 \neq S_2$, the trajectories for states $S_1 = S_2$ are in a plane, as Δi_{LN} does not vary.

III. STEADY STATE BEHAVIOR

For transients, the switching states $S_1 = S_2$ should be used and the interleaved converter will behave like a standard boost. At steady state, the SIBC alternates the switching states $S_1 \neq S_2$ to minimize the current ripple, and this is the same as selecting the most propitious switching sequence. In order to make the previous equations analytically tractable at steady state, it is considered that $v_{cN} \approx V_{ON}$ [11] with V_{ON} the average value of the output voltage. This is the same as assuming a proper voltage regulation [11] with the currents being straight lines. With $v_{cN} \approx V_{ON}$, the trajectory for $S_1 = S_2 = 0$ is a parabola approximating the circle (4) according to [12]:

$$S_1 = S_2 = 0 : \quad v_{cN} = v_{BN} - \frac{i_{LN}^2 - i_{BN}^2}{2(V_{ON} - V_{IN})} + I_{ON} \frac{i_{LN} - i_{BN}}{V_{ON} - V_{IN}} \quad (10)$$

For $S_1 = 1, S_2 = 0$, the trajectory is also a parabola by approximating the complex curve resulting from solving (8)-(9):

$$\begin{aligned} S_1 = 1, S_2 = 0 : v_{cN} &= v_{cNB} \\ &+ \frac{(i_{LN} - i_{LNB})V_{ON}(\Delta i_{LNB} - i_{LN} + 2I_{ON})}{(2V_{IN} - V_{ON})^2} \\ &- \frac{V_{IN}(i_{LN} - i_{LNB})(2\Delta i_{LNB} - i_{LN} - i_{LNB} + 4I_{ON})}{(2V_{IN} - V_{ON})^2} \end{aligned} \quad (11)$$

The solution for $S_1 = 0, S_2 = 1$ is the same as the solution for (11) but with the opposite sign for Δi_{LNB} . Moreover, Δi_{LN} increases linearly with slope $V_{ON}/2$ for $S_1 = 1, S_2 = 0$ and decreases for $S_1 = 0, S_2 = 1$,

$$\Delta i_{LN} = \Delta i_{LN}(0) \pm \frac{V_{ON}t_N}{2} \text{ for } S_1 = 1/0, S_2 = 0/1 \quad (12)$$

For $V_{IN} < V_{ON}/2$, the sequences $S_1 \neq S_2$ result in decreased i_{LN} , see Fig. 5, just as occurs with $S_1 = S_2 = 0$. Hence, the switching states used in this case are $S_1 \neq S_2$ to decrease i_{LN} and $S_1 = S_2 = 1$ to increase it. The state $S_1 = S_2 = 0$ will not be used during the steady state. The points B and A of maximum and minimum current correspond to the final and initial application of $S_1 = S_2 = 1$ respectively, see Fig. 5. The trajectories for $V_{IN} > V_{ON}/2$ are too close to be discerned in a practical implementation, and will not be considered.

To calculate the relation between point B (I_{LNB}, V_{cNB}) in the phase plane with minimum current and the average point (I_{LN}, V_{ON}), see Fig. 5, three conditions must be met, namely, the power must be conserved, and the average current and voltage must be calculated [15]:

$$V_{IN}I_{LN} = V_{ON}I_{ON} \quad (13)$$

$$I_{LN} = \frac{1}{T_{sw}} \left(\underbrace{\int_0^{T_{B \rightarrow A}} i_{LN} dt}_{B \rightarrow A(S_1=1, S_2=0)} + \underbrace{\int_0^{T_{A \rightarrow B}} i_{LN} dt}_{A \rightarrow B(S_1=1, S_2=1)} \right) \quad (14)$$

$$V_{ON} = \frac{1}{T_{sw}} \left(\underbrace{\int_0^{T_{B \rightarrow A}} v_{cN} dt}_{B \rightarrow A(S_1=1, S_2=0)} + \underbrace{\int_0^{T_{A \rightarrow B}} v_{cN} dt}_{A \rightarrow B(S_1=1, S_2=1)} \right) \quad (15)$$

with $T_{B \rightarrow A}$ the time corresponding to descending current, see Fig. 5, from B to A . Finally, T_{swN} is the switching period. Therefore, for $B \rightarrow A$, the solutions for the equations $S_1 \neq S_2$ (6)-(7) must be used and for $A \rightarrow B$, the solution for equation $S_1 = S_2 = 1$ equation (3) must be used. Additionally, it is necessary to use the condition of periodicity $i_{LN}(T_{swN}) = i_{LNB}$ [15]. Finally, in steady state the average value of Δi_{LNB} must be null. Because the switching states $S_1 \neq S_2 = 1$ are applied alternately, the following constraint must be fulfilled:

$$0 = \frac{1}{2T_{sw}} \left(\underbrace{\int_0^{T_{B \rightarrow A}} \Delta i_{LN} dt}_{B \rightarrow A(S_1=1, S_2=0)} + \underbrace{\int_0^{T_{B \rightarrow A}} \Delta i_{LN} dt}_{B \rightarrow A(S_1=0, S_2=1)} \right) \quad (16)$$

After some algebraic operations, the relation between the average values and the values at the extreme point B is:

$$V_{cNB} = V_{ON} + \frac{I_{ON} T_{swN} (2V_{IN} - V_{ON})}{2V_{ON}} + \frac{T_{swN}^2 V_{IN}^3 (V_{IN} - V_{ON})}{3V_{ON}^3} \quad (17)$$

$$I_{LNB} = \frac{I_{ON} V_{ON}}{V_{IN}} - \frac{T_{swN} V_{IN} (2V_{IN} - V_{ON})}{2V_{ON}} \quad (18)$$

The maximum Δi_{LNB} corresponding to the points B and A is:

$$\Delta i_{LNBmax} = \pm \frac{T_{swN} V_{IN}}{2} \quad (19)$$

IV. CONTROL LAW DERIVATION

Boundary control uses the solution of the previous equations to establish an appropriate control law to achieve an outstanding dynamic response. For large transients, with v_{cN} far from the reference V_{ON} , the boost converter should react as quickly as possible. This is achieved by using the switching states $S_1 = S_2$ as a simple boost converter. The switching surface is determined by the circle (4) and the straight line (5) intersecting at the point B , the coordinates of which are calculated in (17)-(18), see Fig. 6. Therefore, the first control law is:

$$\begin{aligned} v_{cN} < v_{cNL} \text{ or } v_{cN} > v_{cNH} : \\ \text{if } (v_{cN}, i_{LN}) \in L \text{ then } S_1 = S_2 = 1 \\ \text{else } S_1 = S_2 = 0 \end{aligned} \quad (20)$$

Note again that, during these switching actions, Δi_{LN} remains constant. Finally, when the trajectory is near the ripple area in the state plane, see the shaded area s in Fig. 6, the switching state $S_1 = S_2 = 0$ is replaced by the sequence $S_1 \neq S_2$ alternating after $S_1 = S_2 = 1$ to make the average Δi_{LN} null:

$$\begin{aligned} v_{cN} > v_{cNL} \text{ and } v_{cN} < v_{cNH} : \\ \text{if } (i_{LN}, v_{cN}) \in S \text{ then } S_1 = S_2 = 1 \\ \text{else alternate } S_1 = 0/1, S_2 = 1/0 \end{aligned} \quad (21)$$

In order to prevent chatter, a hysteresis band is needed for v_{cNL} and v_{cNH} . It can be seen that, despite the additional state in the equations of the SIBC, the control law is simple to implement.

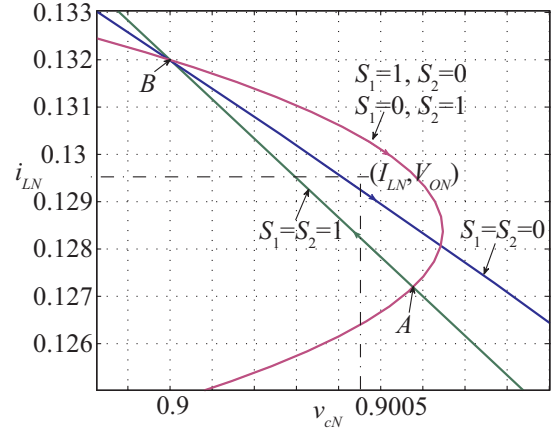


Fig. 5. Trajectories near the operating point for $V_{IN} < v_{cN}/2$.

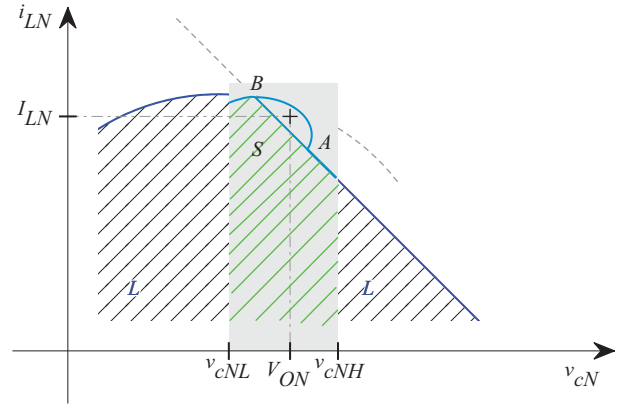


Fig. 6. Representation of the control law.

V. SIMULATION AND EXPERIMENTAL RESULTS

Table I contains the parameters for the simulations and the experiments, based on a converter operating at moderate switching frequency. Figs. 7 and 8 show the simulation results for a full-load step, achieving very fast transient even in the presence of low switching frequency. The switching devices are modeled ideally. The ESR of the passive elements, and the quantization effects of the ADCs, are considered. Fig. 7 shows the signals in the time domain, and Fig. 8 shows the phase plane for i_{LN} and v_{cN} . The actions of the boundary control allow for a very fast transient with very low dip in the output voltage even when using small film capacitors. It can also be seen that, after the transient, the interleaving switching sequence resumes with very close to constant switching frequency during steady state.

Figs. 9 and 10 show the experimental results for the same previous full-load step. All the calculations of the simple control law are performed using a low cost DSP (TMS320F28335 by Texas Instruments), which was programmed using C. Fig. 9 shows the signals in the time domain, and Fig. 10 the phase plane for v_{cN} and i_{LN} . It can be seen that the experimental results are very similar to the previous simulation results, and

TABLE I
PARAMETERS FOR THE SIMULATIONS AND EXPERIMENTS

Parameter	Value
Rated power P_n	60 W
Output voltage V_O	25 V
Input voltage V_I	10 V
Switching frequency f_{sw}	3 kHz
Sampling frequency f_s	300 kHz
Inductor inductance L	0.508 mH
Inductor resistance R_L	36 mΩ
Capacitor capacitance C	141 μF
Capacitor resistance R_C	9.7 mΩ

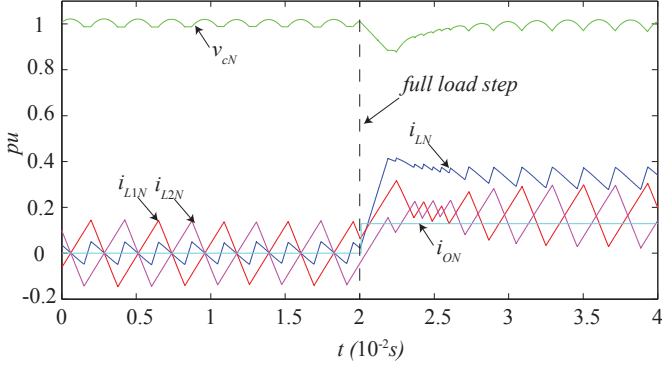


Fig. 7. Simulation results for the full load step: time domains signals v_{cN} , i_{LN} , i_{L1N} , i_{L2N} and load current i_{ON} .

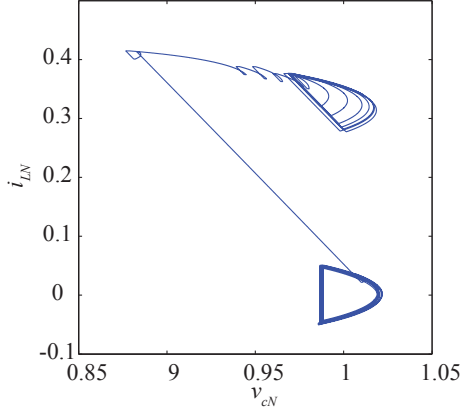


Fig. 8. Simulation results for the full load step: phase plane for v_{cN} and i_{LN} .

show a very fast response. The bandwidth limitation of the sensors and the full losses of the passive elements have a smoothing effect on the control magnitudes. This leads to fewer switching actions after the load step comparing to the simulations. It also leads to small variations in the switching actions during steady state with approximately constant switching frequency.

VI. CONCLUSION

This paper has introduced the boundary control of the synchronous interleaved boost inverter. This control strategy

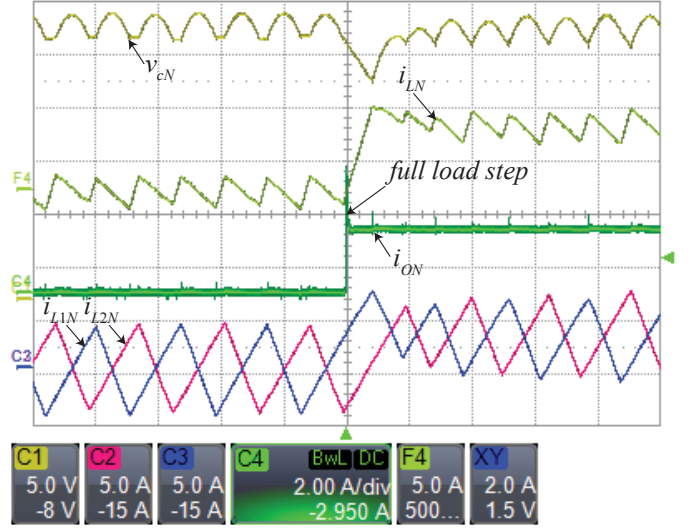


Fig. 9. Experimental results for the full load step: time domains signals v_{cN} , i_{LN} , i_{L1N} , i_{L2N} .

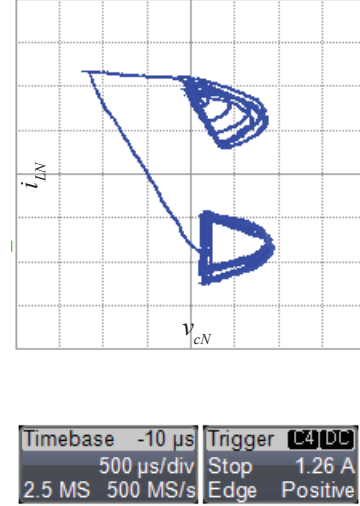


Fig. 10. Experimental results for the full load step: phase plane for v_{cN} and i_{LN} .

enables the non-minimum phase problems of the boost converter to be overcome, and it is able to cope with the presence of additional switches and passive elements in the interleaved converter. The derived control law is simple and is based on the rigorous analysis of the exact trajectories during transients, and approximate trajectories during steady state. The simulations and experiments, which are fully consistent with the theoretical derivations, validate the proposals and result in very fast transients for full-load step variations.

REFERENCES

- [1] R. W. Erickson and D. Maksimovic, *Fundamentals of Power Electron.* Springer, 2001.
- [2] Z. Chen, W. Gao, J. Hu, and X. Ye, "Closed-loop analysis and cascade control of a nonminimum phase boost converter," *IEEE Trans. Power Electron.*, vol. 26, no. 4, pp. 1237–1252, April 2011.

- [3] L. Martinez-Salamero, G. Garcia, M. Orellana, C. Lahore, and B. Estibals, "Start-up control and voltage regulation in a boost converter under sliding-mode operation," *IEEE Trans. Ind. Electron.*, vol. 60, no. 10, pp. 4637–4649, Oct 2013.
- [4] S. Oucheriah and L. Guo, "Pwm-based adaptive sliding-mode control for boost dc-dc converters," *IEEE Trans. Ind. Electron.*, vol. 60, no. 8, pp. 3291–3294, Aug 2013.
- [5] R. Giral, L. Martinez-Salamero, R. Leyva, and J. Maixe, "Sliding-mode control of interleaved boost converters," *IEEE Trans. Circuits and Systems I: Fundamental Theory and Applicat.*, vol. 47, no. 9, pp. 1330–1339, Sep 2000.
- [6] R. Munzert and P. T. Krein, "Issues in boundary control [of power converters]," in *IEEE PESC '96*, vol. 1, Jun 1996, pp. 810–816 vol.1.
- [7] M. Greuel, R. Muyschondt, and P. T. Krein, "Design approaches to boundary controllers," in *IEEE PESC '97*, vol. 1, Jun 1997, pp. 672–678 vol.1.
- [8] G. E. Pitel and P. T. Krein, "Trajectory paths for dc - dc converters and limits to performance," in *IEEE COMPEL '06*, July 2006, pp. 40–47.
- [9] P. T. Krein, *Elements Of Power Electronics*. Oxford University Press, Incorporated, 1998.
- [10] G. M. Buiatti, J. A. Martin-Ramos, C. H. R. Garcia, A. Amaral, and A. J. Marques Cardoso, "An online and noninvasive technique for the condition monitoring of capacitors in boost converters," *IEEE Trans. Instrumentation and Measurement*, vol. 59, no. 8, pp. 2134–2143, Aug 2010.
- [11] W. W. Burns and T. G. Wilson, "State trajectories used to observe and control dc-to-dc converters," *IEEE Trans. Aerosp. Electron. Syst.*, vol. AES-12, no. 6, pp. 706–717, Nov 1976.
- [12] —, "A state-trajectory control law for dc-to-dc converters," *IEEE Trans. Aerosp. Electron. Syst.*, vol. AES-14, no. 1, pp. 2–20, Jan 1978.
- [13] J. M. Galvez, M. Ordonez, F. Luchino, and J. E. Quaicoe, "Improvements in boundary control of boost converters using the natural switching surface," *IEEE Trans. Power Electron.*, vol. 26, no. 11, pp. 3367–3376, Nov 2011.
- [14] T.-T. Song and H. S. H. Chung, "Boundary control of boost converters using state-energy plane," *IEEE Trans. Power Electron.*, vol. 23, no. 2, pp. 551–563, March 2008.
- [15] W. W. Burns, "A theory of control for a class of electronic power processing systems," Ph.D. dissertation, Duke Univ., Durham, NC., April 1977.